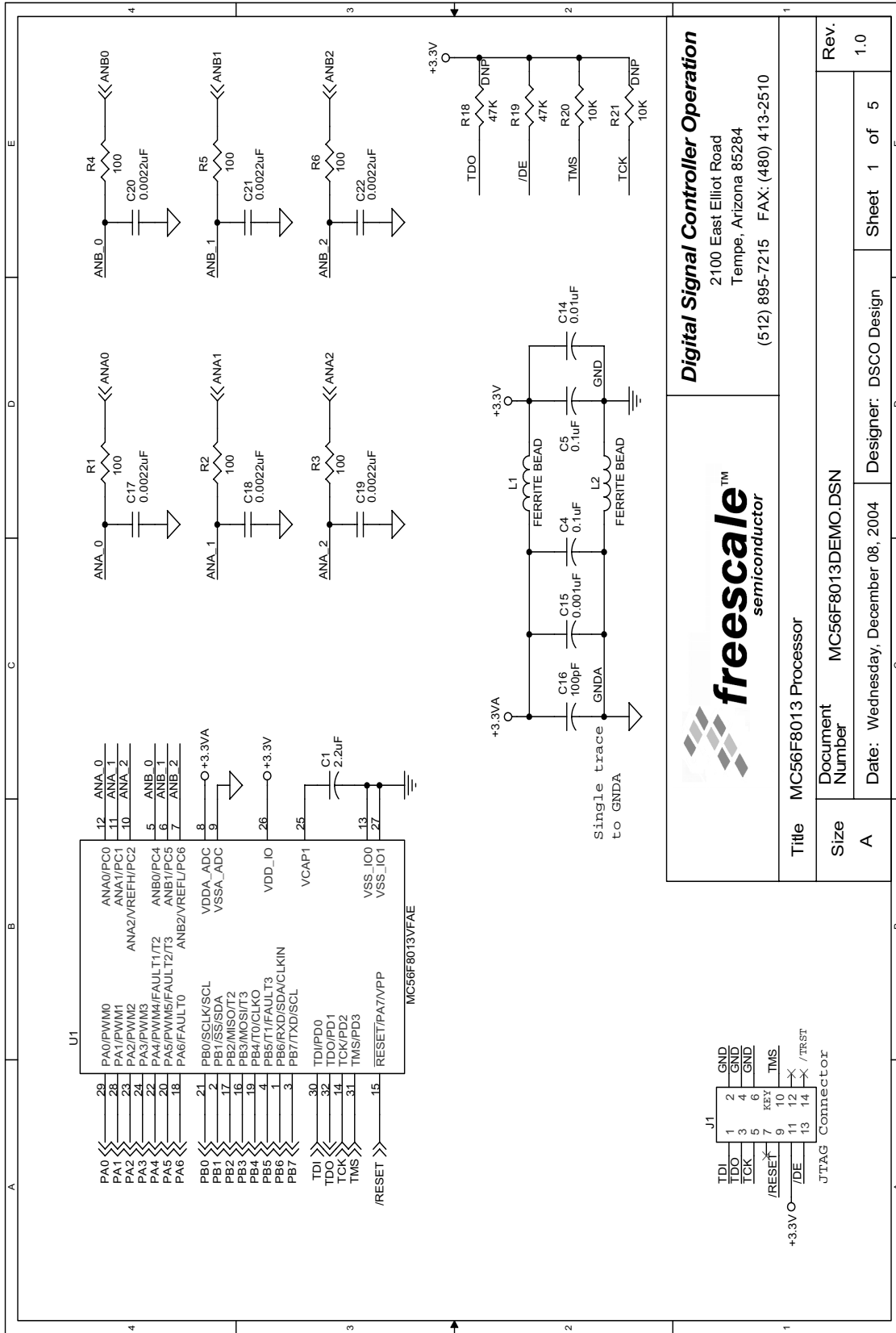




Appendix A

56F8013 Demonstration Board Schematics

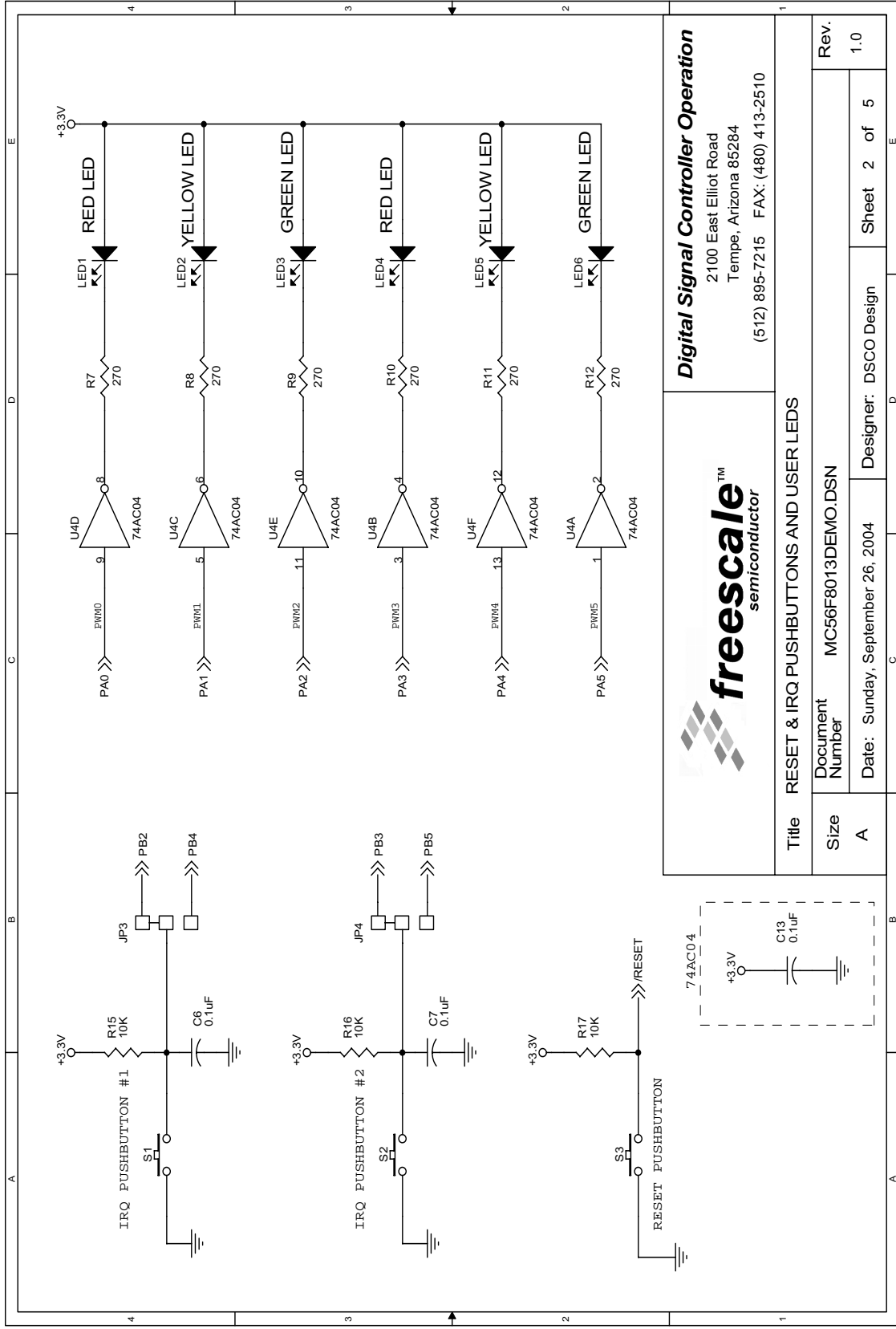


freescale
semiconductor

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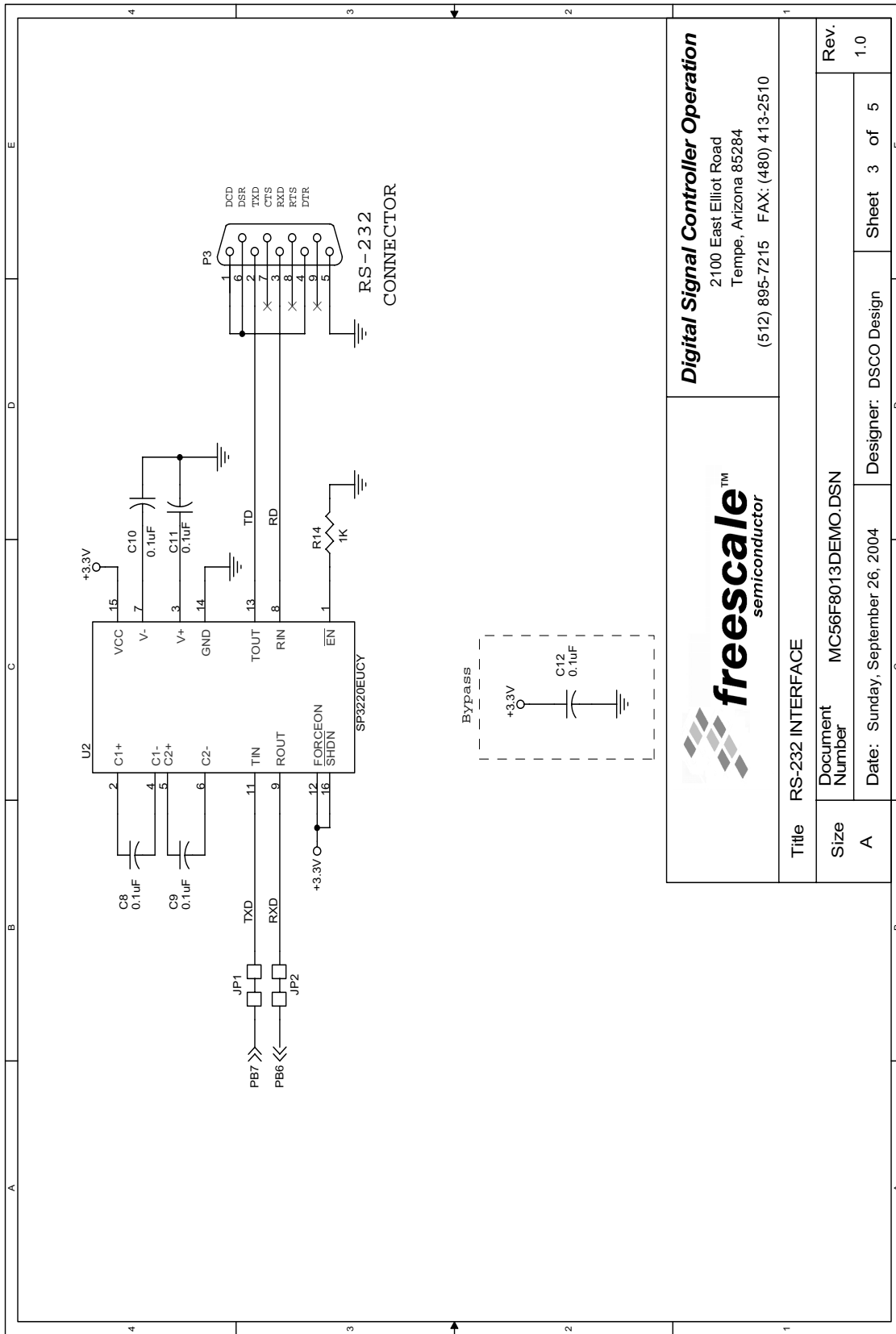
Title MC56F8013 Processor	
Document Number MC56F8013DEMO.DSN	Designer: DSCO Design
Date: Wednesday, December 08, 2004	Sheet 1 of 5
Size A	Rev. 1.0

Figure A-1. MC56F8013 PROCESSOR



		Digital Signal Controller Operation 2100 East Elliot Road Tempe, Arizona 85284 (512) 895-7215 FAX: (480) 413-2510	
		Title RESET & IRQ PUSHBUTTONS AND USER LEDs	Rev. 1.0
Size A	Document Number MC56F8013DEMO.DSN	Designer: DSCO Design	Sheet 2 of 5
Date: Sunday, September 26, 2004			

Figure A-2. RESET & IRQ Pushbuttons and User LEDs




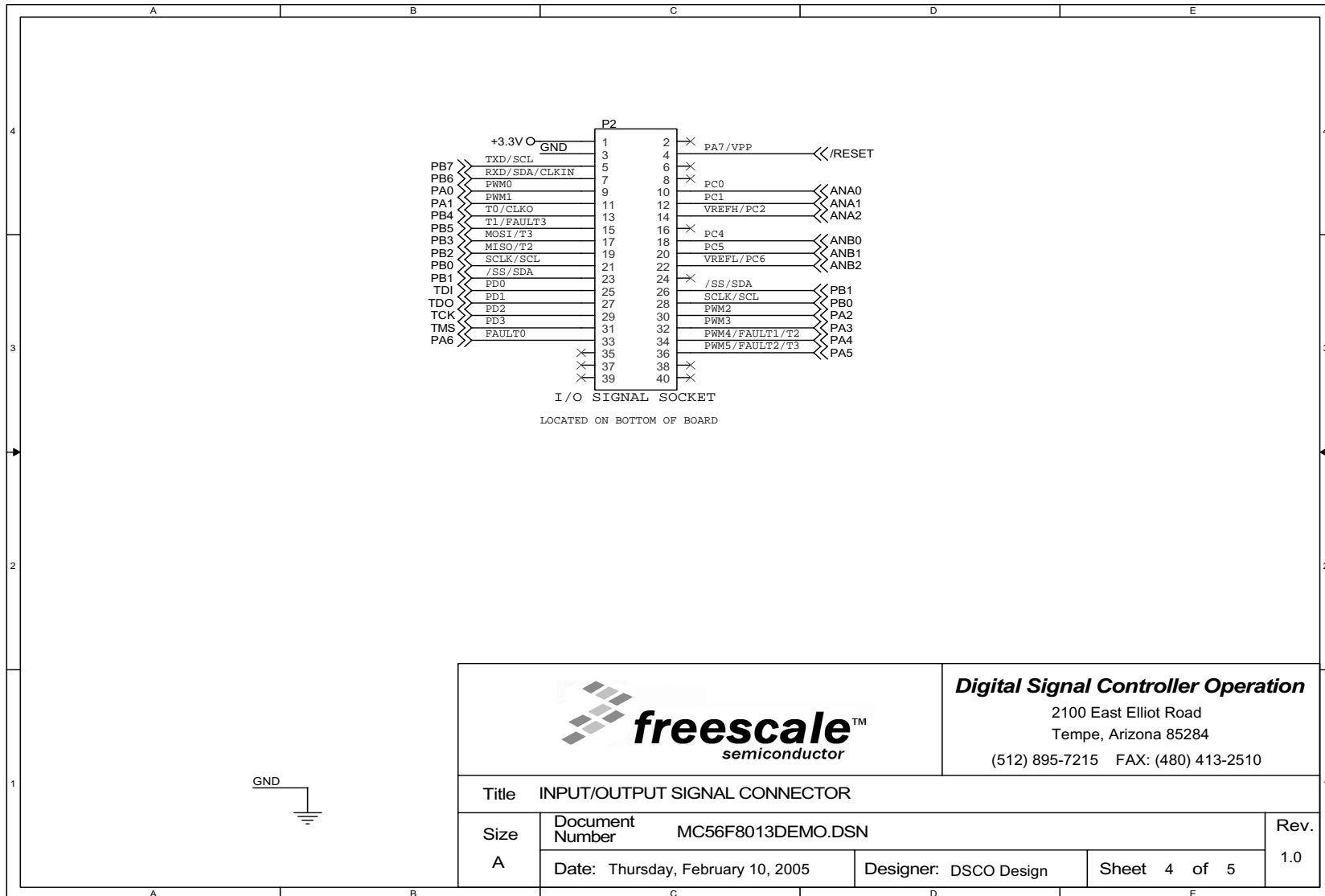
		Digital Signal Controller Operation 2100 East Elliot Road Tempe, Arizona 85284 (512) 895-7215 FAX: (480) 413-2510	
		Title RS-232 INTERFACE	
Size A	Document Number MC56F8013DEMO.DSN	Date: Sunday, September 26, 2004	Designer: DSCO Design
		Sheet 3 of 5	Rev. 1.0

Figure A-3. RS-232 Interface



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Title		INPUT/OUTPUT SIGNAL CONNECTOR		Rev. 1.0
Size	Document Number	MC56F8013DEMO.DSN		
A	Date: Thursday, February 10, 2005	Designer: DSCO Design	Sheet 4 of 5	

Figure A-4. Input / Output Signal Connector

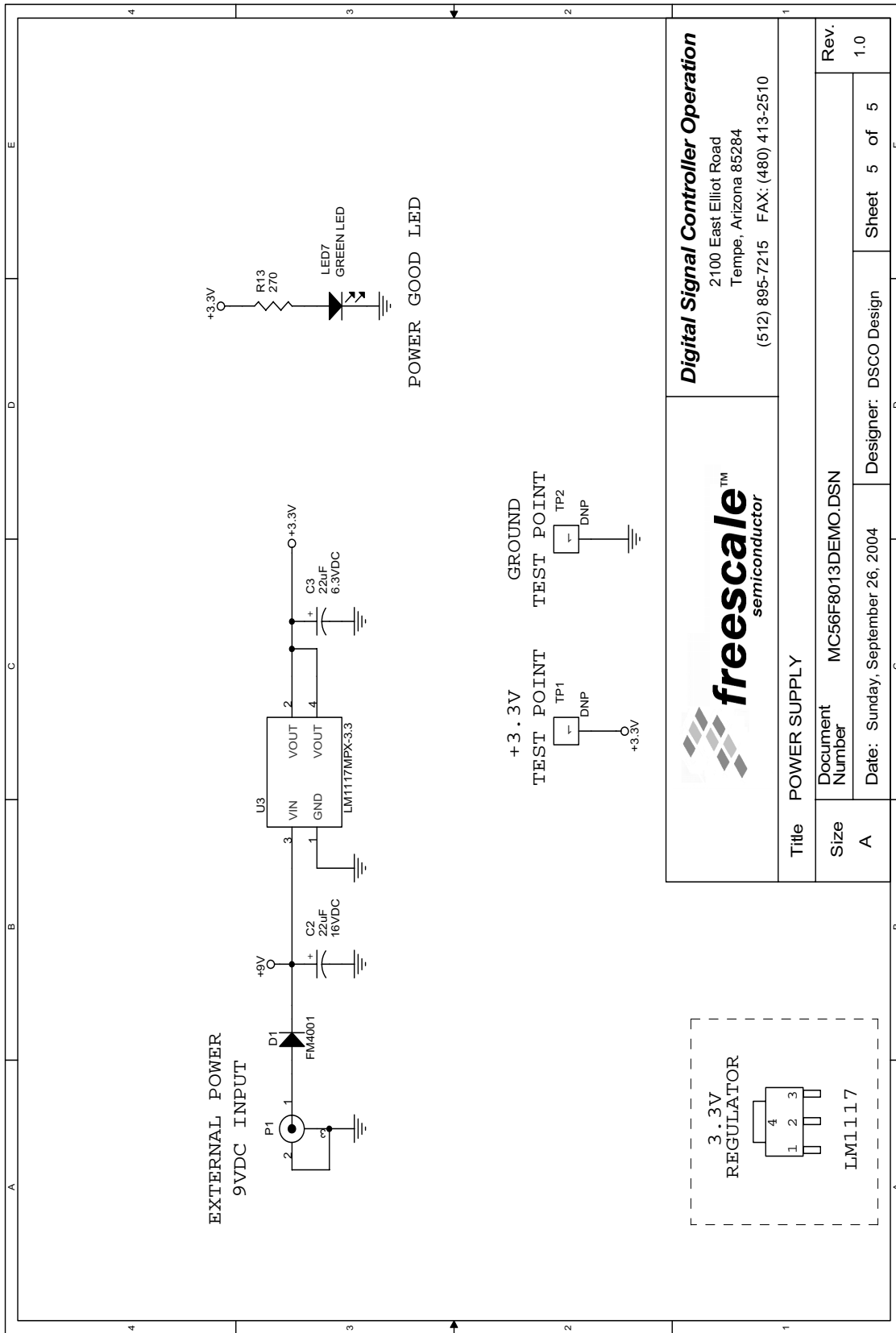


Figure A-5. Power Supply