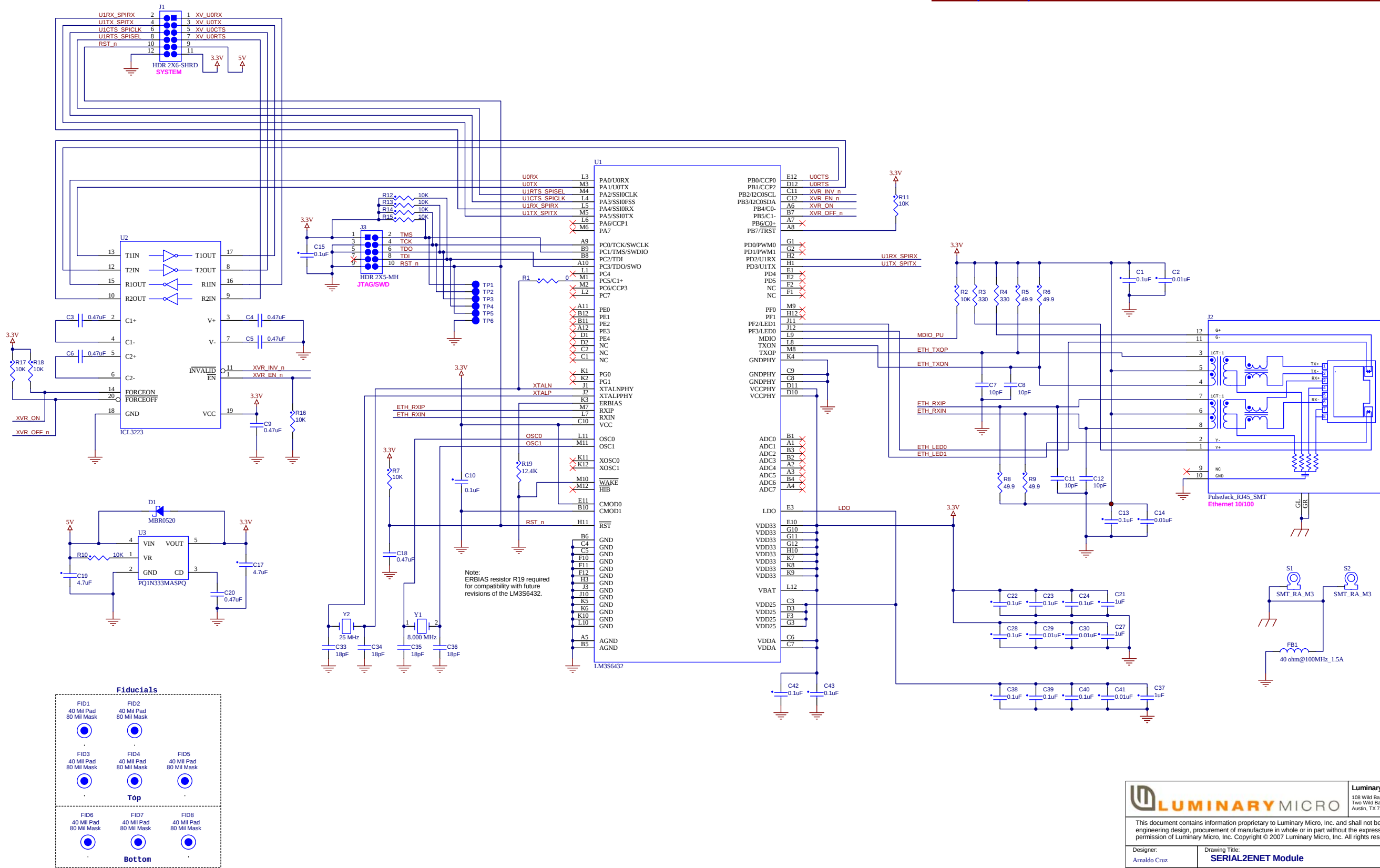


Revision History

Revision	Date	Description
X0	1/16/2008	First release.
X1	1/18/2008	Second release.
X2	1/21/2008	Added JTAG pullups.
X3	1/25/2008	Added pulldown to U2 pin 1 to enable operation with ICL3223.
RA	1/26/2008	Released for manufacturing.
RB	2/12/2008	Fixed S1,S2 pads, rotated J3, fixed net names.
RC	4/2/2008	Added pulldown resistor to U1-K3, added pullups to U2 to enable transceiver after reset. Renamed GNDPHY (U1-K3) to ERBIAS, fixed termination for ethernet RX signals. Added JTAG test pads.





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Designer: Arnaldo Cruz	Drawing Title: SERIAL2ENET Module		
Drawn by: Arnaldo Cruz	Page Title: FURY BGA108, UART Transceiver, RJ45 with Magnetics		
Approved: *	Size C	Document Number: 0001	Rev RC
Date: 4/25/2008		Sheet 1 of 1	