

	Family	Market Focus	Application Examples	Flash	ROM Available	RAM	EEPROM	CAN	Analog (ADC)	UART	SPI	PC	SLIC	Timer	Clock	Pin Count	Additional Features
High End	DZ	CAN, LIN Master, General Market	Wireless Control, Rx Low End, HVAC Door Modules, Watchdog Ctrl, Power Lift Gate	16 KB–128 KB		1 KB–8 KB	512B–2KB	1	Up to 24-ch., 10-bit ADC, two comparators	2 x SCI	Up to 2	Up to 2		Up to 10-ch.+2-ch.	MCG (FLL, FLL, OSC)	100, 64, 48, 32	40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor
	DV	CAN, LIN Master, General Market		16 KB–128 KB	✓	1 KB–6 KB		1	Up to 24-ch., 10-bit ADC, two comparators	2 x SCI	Up to 2	Up to 2		Up to 10-ch.+2-ch.	MCG (FLL, FLL, OSC)	100, 64, 48, 32	40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor
	DN	LIN Master, General Market		16 KB–60 KB		1 KB–2 KB	512B–2 KB		Up to 16-ch., 10-bit ADC, two comparators	1 x SCI	1	1		Up to 6-ch.+2-ch.	MCG (FLL, FLL, OSC)	64, 48, 32	40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor

Mid Range	EN	General Market	Trip Computer Seats, Rain Sensors, Compass, Mirror	16 KB–32 KB	✓	512B–1 KB			Up to 12-ch., 10-bit ADC, one comparator	1 x SCI	1			4-ch.	MCG (FLL, FLL, OSC)	48, 32	40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor
	EL	LIN Slave, General Market		16 KB–32 KB		1 KB	Up to 512B		Up to 16-ch., 10-bit ADC, two comparators	1 x SCI	1	1	1	4-ch.+2-ch.	ICS	28, 20	LIN Auto-Baud/Synch, 40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor
	AW	LIN Slave, High I/O	Trip Computer Seats, Rain Sensors, Compass, Mirror	8 KB–60 KB	✓	768B–2 KB			Up to 16-ch., 10-bit ADC	Up to 2 x SCI	1	1		Up to 6-ch.+2-ch.	ICG	64, 48, 44, 32	40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor

Low End	SL	LIN Slave, General Market	LIN Sub-Bus, Watchdog Ctrl, Airbag, General Powertrain	8 KB–16 KB		512B	Up to 256B		Up to 16-ch., 10-bit ADC, one comparator	1 x SCI	1	1	1	2-ch.+2-ch.	ICS	28, 20	LIN Auto-Baud/Synch, 40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor
	SG	LIN Slave, General Market		4 KB–32 KB	✓	256B–1 KB			Up to 16-ch., 10-bit ADC, one comparator	1 x SCI	1	1		Up to 2-ch.+2-ch.	ICS	28, 20, 16, 8	40 MHz CPU, Watchdog OSC/Timer, COP, LVI, ICE, BDM, POR, KBI, Temp Sensor

ADC Analog-to-Digital Converter

ROM Read-Only Memory

RAM Random Access Memory

EEPROM Electrically Erasable Programmable Read-Only Memory

CAN Controller Area Network

LIN Local Interconnect Network

ICG Internal Clock Generator

ICS Internal Clock Source

UART Universal Asynchronous Receiver Transmitter

SPI Serial Peripheral Interface

PC Inter-Integrated Circuit

SLIC Slave LIN Interface Controller

MCG Multipurpose Clock Generator

PLL Phase-Locked Loop

FLL Frequency-Locked Loop

OSC Oscillator on-Chip

COP Computer Operating Properly

LVI Low Voltage Inhibit

ICE In-Circuit Emulation

BDM Background Debug Mode

POR Power-on-Reset

KBI Keyboard Interrupt Module